



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

S. Shukuri, et al.

Application. No.

10/635,511

Filed:

August 7, 2003

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING

DEPOSITED LAYER FOR GATE INSULATION

Group:

2814

Examiner:

L. Pham

SUPPLEMENTING AMENDMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 October 21, 2004

Sir:

Supplementing the Response filed August 20, 2004, please amend the above-identified application as listed in the following, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.